**Ievgen Verbytskyi**

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**Personal information**

**Adress**: 37, Peremogy av.,

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**National Technical University of Ukraine**

**“Kyiv Polytechnic Institute”**

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**Date of birth**: 01.03.1984.

**Nationality**: Ukraine.

**Education**

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| 2001 – 2007  | National Technical University of Ukraine “Kyiv Polytechnic Institute” , Kyiv, Ukraine  |
| 2009 – 2013  | PhD - Thesis Title: Predict control with parameters identification DC-DC converters. Kyiv Polytechnic Institute Doctoral project:- Applying two dimensional Fourier series for analyze of spectrum of modulating signals  |
| 2002 –2007 | M.Sc. - Thesis Title: Calculation algorithms of transient and steady state processes with free mode functions development. Kyiv Polytechnic Institute |

**Work experience**

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| 2011 – 2016  | Associate professor. Department of industrial electronics, Faculty of Electronics.National Technical University of Ukraine “Igor Sikorsky Kyiv Polytechnic Institute”37 Prospect Peremogy, 03056 Kyiv, Ukraine <http://www.kaf-pe.kpi.ua/en/index.php> |
| 2011 – 2015  | Senior lecturer. Department of industrial electronics, Faculty of Electronics.National Technical University of Ukraine “Kyiv Polytechnic Institute”37 Prospect Peremogy, 03056 Kyiv, Ukraine <http://www.kaf-pe.kpi.ua/en/index.php> |
| 2007 – 2011 | Engeneer. **State enterpise “State Kyiv design bureau “Luch”**2 Melnikov Str., 04050 Kyiv,Ukraine Phone: +38 (044) 483 0745, 484 1898 Fax: +38 (044) 483 1394E-mail: kb@luch.kiev.ua[www.luch.kiev.ua](http://www.luch.kiev.ua/)Requirements* Electronic system design;
* Tuning and maintence of electronic equipment.

Work on technical support held in Ukraine and abroad: in Slovakia, Azerbaijan, Vietnam, Libya |
| 2003 – 2006 | Laboratory Assistant.Department of general and theoretical physics, National Technical University of Ukraine “Kyiv Polytechnic Institute”.System administrator. |

**Publications, patents**

Author or co-author of 60 papers published in Ukrainian and foreign scientific journals, participant of 15 international scientific conferences. Co-inventor of 6 patents.

**Language skills**

Mother tongues: Russian, Ukrainian.

Other languages: English - middle level.

**Software tool knowledge**

- Matlab (simulating electronics schemes, realising control systemalgorithms);

- Mathematica (mathematical models development);

- AVR tools, Coocox IDE (microcontrollers programming);

- C compilers (software development);

- Altium Designer (electronics scheme and printed circuit board develop).